

Application Number

10 / 604 213

PETER BERASI, ET AL.

7/01/03

1763

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

FIS919990286US1-SERIAL NO. 09/596,754 (has been abandoned)

Technical Report:"ESSONNES TIN CAP PROCESS DEVELOPMENT SUMMARY" NOV.24, 1994, 95A001149
Technical Report:"TEMPORARY CHIP ATTACH TECHNIQUE FOR BURN IN OF CMOS CHIPS WITH TIN CAP PADS",

EXAMINER

DATE CONSIDERED

11/14/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.